IN THE UNITED STATES PATENT AND PRADEMARK OFFICE

APPLICANTS:

Takayuki Gomi et al.

SERIAL NO: 08/965,2

08/965,286 \ JUL '

OUP ART UNIT:

TORNEXIDOCKET NO.: P97,26084

2811

FILED:

November 6, 1997

EXAMINER:

W. Fahmy

INVENTION:

BIPOLAR TRANSISTOR AND METHOD OF THE SAME

AMENDMENT

Assistant Commissioner of Patents Washington, D.C. 20231 S I R:

In response to the Office Action mailed on April 7, 1999, Applicants herein amend the above-identified application and respond as follows:

In the Claims:

Please amend claim 1 as follows:

1. (Amended) A semiconductor device, comprising a first vertical type high speed NPN bipolar transistor and a second vertical type high voltage NPN bipolar transistor whose breakdown voltage is higher than [different from] that of said first vertical type bipolar transistor, [formed on a semiconductor substrate made by forming] the device including an epitaxial layer formed on a silicon substrate, wherein said first vertical type bipolar transistor has a first embedded diffusion layer formed in an upper part of said silicon substrate, said first vertical type bipolar transistor having [and of] the same conductive type as said epitaxial layer and having an impurity concentration higher than that of said epitaxial layer, said second vertical type bipolar transistor has a second embedded diffusion layer formed in an upper part of said silicon substrate, said second vertical type bipolar transistor having [and of] the same conductive type as said epitaxial layer[,] and [the] having an impurity concentration [of said second embedded diffusion layer is different from] less than the impurity concentration of said first embedded diffusion layer.

Please cancel claim 2, without prejudice.

Please amend claim 3 as follows:

3. (Amended) A semiconductor device according to claim 1, wherein <u>a</u> [the] depth of said first embedded diffusion layer is shallower than <u>a</u> [the] depth of said second embedded diffusion layer.

Please amend claim 4 as follows:

In line,1, cancel "claim 3" and substitute therefor --claim 1--.

Please cancel claim 5, without prejudice.

Please amend claim 6 as follows:

In line 1, cancel "claim 3" and substitute therefor --claim 1--.

Please cancel claims 7-10, without prejudice.

Please amend claim 11 as follows:

Col of which



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In re application of:

HILL & SIMPSON

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RIPOLAR TRANSISTOR AND METHOD OF THE SAME

For:	BIPOL	AR IRANS	SISTOR AND MET	HOD OF THE	SAME		
			AMENDME	<u>NT</u>		•	-
Assistant Commissioner for Patents Washington D.C. 20231 SIR: Transmitted herewith is an amendment in the above-identified application. □ No additional fee is required. The fee has been calculated as shown below.						JUL 16 TC 2800 MAII	RECEIV
CLAIMS AS AMENDED							7円
	(2) CLAIMS REMAINING AFTER AMENDMENT		(4) HIGHEST NO. PREVIOUSLY PAID FOR	(5) PRESENT EXTRA	(6) RATE	ADD H ONAL FEE	
TOTAL CLAIMS		MINUS			() X 9.00 () X 18.00		
INDEP. CLAIMS		MINUS	·		() X 39.00 () X 78.00].
Application amended to contain any multiple dependent claims not previously paid for.				() YES () NO	() \$130.00 () \$260.00 ONE TIME		
TOTAL ADDITIONAL FOR THIS AMENDA						-0-]
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			William I. Vaugha		CANTE	ation No. 39,056	<u>.</u>

July 7, 1999

DATE